

Youssef Mansour

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Education

- Ain Shams University – Faculty of Engineering
B.Sc. in Communication and Electronics Engineering (Sep 2020 – Jun 2025)
Graduated with GPA: 2.81
- Motafwkeen Ain Shams High School
Graduated with score: 97% (Sep 2017 – Jul 2020)

Technical Skills

- Programming & Hardware Description Languages: C, C++, Verilog, SystemVerilog, MATLAB
- EDA & Tools: Design Compiler, RTL Synthesis, Static Timing Analysis, DFT, UVM
- Embedded Systems: AVR Microcontroller, RTOS, Automotive Bus Technologies
- Networking & Communication: Wireless Communication, Fiber Optics, Microwave & Broadband, Networking Essentials
- Other Skills: Time Management, Adaptability, Team Collaboration, Microsoft Office

Certifications & Diplomas

- Digital IC Design Diploma – Eng. Ali ElTemsah (Feb 2024 – Aug 2024)
- Digital Verification Diploma – Eng. Kareem Wassem (Sep 2024 – Oct 2024)
- ITI Embedded Systems Diploma (Jul 2023 – Aug 2023)
- FIT EINS Certificate – Goethe Institute (Apr 2019)
- Introduction to Financial Accounting – University of Pennsylvania (Coursera)

Professional Experience & Internships

Systel Telecomm – Internship (Aug 2024 – Sep 2024)

- Trained in wireless communication fundamentals, networks, and broadband systems.
- Gained practical exposure to fiber optics, CCTV, and networking essentials.
- Developed soft skills in communication, presentation, teamwork, and CV writing.

ITI Emdeded System Diploma (Aug 2023 – Sep 2023)

- Developed strong **C programming skills** for embedded systems.
- Gained hands-on experience with **AVR microcontroller interfacing**.
- Worked with **embedded system tooling** (debuggers, simulators, compilers).
- Implemented **Real-Time Operating System (RTOS)** concepts.

The Arab Contractors – Internship (Jun 2022 – Jul 2022)

- Worked on technical content including wireless communication, networking, and fiber optics.
- Participated in practical training on microwave and broadband technologies.

Workshops & Clubs:

- Participant, SCCI 'Investeneur Workshop'
- PR Member, SCCI
- HR Member, Aviation Club

Academic Projects

Graduation Project (Oct 2024 – Jun 2025)

Developing an Ultra-Compact Head for Contactless Sensing with Advanced Optical Structures – in collaboration with Si-Ware

- Designed and developed advanced optical structures for sensing applications.

Digital Verification & IC Design Projects

ASIC PROJECTS: UART - ALU – Traffic Light

VERIFICATION PROJECTS: FIFO

- Built RTL coding projects using Verilog and developed self-checking testbenches.
- Performed static timing analysis, low-power design techniques, and clock domain crossing management.
- Conducted RTL synthesis, DFT, and full ASIC design flow (floorplanning, placement, routing, timing closure).
- Applied UVM methodology: constrained random generation, assertions, functional coverage, and DUT verification.

Military Status

Exempted